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ATTORNEY DOCKET NO.: 040894-3411-01

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:)
Etsuyoshi KOBORI)
Application No. 09/610,148) Group Art Unit: 2814
Filed: July 5, 2000) Examiner: A. Mai
For: METHOD OF FABRICATING)
SEMICONDUCTOR DEVICE, AND)
SEMICONDUCTOR DEVICE)

Commissioner for Patents
Washington, D.C. 20231

Sir:

AMENDMENT

In response to the Non-Final Office Action dated August 2, 2002, the period for response to which extends through November 4, 2002 (November 2, 2002 being a Saturday), please amend the above identified application as follows.

IN THE CLAIMS:

Please amend claims 15-18 as follows:

- C¹
15. (Three Times Amended) A semiconductor device comprising:
- a first wiring layer composed of a plurality of wiring patterns separate from each other embedded on a first insulating layer which covers a surface of a semiconductor substrate;
 - a nonconductive layer contacting and covering the first insulating layer and contacting the first wiring layer, a top of the first wiring layer not higher than a top of the first insulating